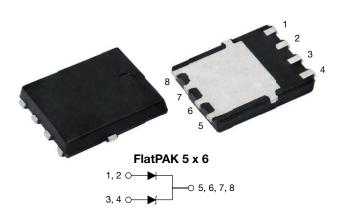


# **Hybrid Solution Surface-Mount**



### **LINKS TO ADDITIONAL RESOURCES**



PRIMARY CHARACTERISTICS					
	I <sub>F(AV)</sub>	3 A			
Standard Rectifier	$V_{RRM}$	600 V			
Standard Rectiller	I <sub>FSM</sub>	40 A			
	$V_F$ at $I_F = 3 \text{ A } (T_J = 125 \text{ °C})$	0.86V			
Transient Voltage Suppressors	$V_{BR}$	27 V			
	$V_{WM}$	23.1 V			
Cuppicosors	P <sub>PPM</sub>	200 W			
T <sub>J</sub> max.	175 °C				
Package	FlatPAK 5 x 6				
Circuit configuration	Common cathode				

#### **FEATURES**

 Automotive hybrid solution for Rectifier and TRANSZORB® TVS



- Oxide planar chip junction
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- ROHS COMPLIANT HALOGEN
- AEC-Q101 qualified available
- Material categorization: for definitions of compliance please see <a href="https://www.vishay.com/doc?99912"><u>www.vishay.com/doc?99912</u></a>

#### **TYPICAL APPLICATIONS**

Secondary protection for sensor units, distributed airbag modules and low power DC / DC converters under power distributer

## **MECHANICAL DATA**

Case: FlatPAK 5 x 6

Molding compound meets UL 94 V-0 flammability rating Base P/NHM3 - halogen-free, RoHS-compliant, and AEC-Q101 qualified

**Terminals:** matte tin plated leads, solderable per J-STD-002 and JESD 22-B102

HM3 suffix meets JESD 201 class 2 whisker test

MAXIMUM RATINGS (T <sub>A</sub> = 25 °C unless otherwise noted)							
TECHNOLOGY	PARAMETER	SYMBOL	R3T2FPHM3	UNIT			
	Device marking code		R3T2FP				
	Maximum repetitive peak reverse voltage	$V_{RRM}$	600	V			
	Maximum DC forward current	I <sub>F(AV)</sub> (1)	3	А			
Standard Rectifier	Maximum DC forward current	I <sub>F(AV)</sub> (2)	2				
	Peak forward surge current 8.3 ms single half sine-wave superimposed on rated load per diode	I <sub>FSM</sub>	40	Α			
Transient Voltage Suppressors	Peak pulse power dissipation with a 10/1000 μs waveform <sup>(3)</sup>	P <sub>PPM</sub>	200	W			
	Peak pulse current with a 10/1000 µs waveform (3)	I <sub>PPM</sub>	5.3	Α			
Operating junction temperature range		T <sub>J</sub> <sup>(4)</sup>	-55 to +175	°C			
Storage temperature range		T <sub>STG</sub>	-55 to +175	°C			

#### Notes

- (1) Mounted on 3 x 3 cm aluminum pad area
- (2) Free air mounted on recommended pad area
- $^{(3)}$  Non-repetitive current pulse per Fig.10 and derated above  $T_A = 25$  °C per Fig.8
- (4) The heat generated must be less than the thermal conductivity from junction-to-ambient:  $dP_D/dT_J < 1/R_{\theta,JA}$



<b>ELECTRICAL CHARACTERISTICS</b> (T <sub>J</sub> = 25 °C unless otherwise noted)								
TECHNOLOGY	PARAMETER	TES	ST CONDITIONS	SYMBOL	MIN.	TYP.	MAX.	UNIT
		I <sub>F</sub> = 1.5 A	T <sub>J</sub> = 25 °C		-	0.91	-	V
	Instantaneous forward voltage	$I_F = 3A$	T <sub>J</sub> = 25 °C	V <sub>F</sub> (1)	1	0.97	1.1	
	instantaneous forward voltage	I <sub>F</sub> = 1.5 A	T <sub>J</sub> = 125 °C	V <sub>F</sub> (··)	-	0.79	-	
Standard Rectifier		$I_F = 3 A$	T <sub>J</sub> = 125 °C		-	0.86	0.98	
Standard nectiller	Reverse current	Rated V <sub>R</sub>	T <sub>J</sub> = 25 °C	I <sub>R</sub> <sup>(2)</sup>	-	-	10	μА
			T <sub>J</sub> = 125 °C		-	13	100	
	Typical reverse recovery time	I <sub>F</sub> = 0.5 A, I <sub>R</sub> = 1.0 A, I <sub>rr</sub> = 0.25 A		t <sub>rr</sub>	-	1.5	-	μs
	Typical junction capacitance	4.0 V, 1 MHz		CJ	-	19	-	pF
	Breakdown Voltage (3)	I <sub>T</sub> =1.0 mA		$V_{BR}$	25.7	27.0	28.4	V
	Stand-off Voltage			$V_{WM}$	-	23.1	-	V
Transient Voltage Suppressors	Maximum Reverse Leakage	Rated V <sub>WM</sub>		I <sub>D</sub>	-	-	0.5	μΑ
	Maximum Clamping Voltage	I <sub>PPM</sub> = 5.3 A, 10/1000 μs waveform		V <sub>C</sub>	-	-	37.5	V
	Typical junction capacitance	0 V		CJ		330		pF
	Typical junction capacitance	23.1 V		CJ		95		pF

#### **Notes**

(1) Pulse test: 300 µs pulse width, 1 % duty cycle

(2) Pulse test: pulse width ≤ 5 ms

(3) Pulse test:  $t_p \le 50 \text{ ms}$ 

IMMUNITY TO ELECTRICAL STATIC DISCHARGE TO THE FOLLOWING STANDARDS ( $T_A = 25~^{\circ}\text{C}$ unless otherwise noted)									
STANDARD	TEST TYPE	TEST TYPE TEST CONDITIONS SYMBOL CLASS VALUE							
AEC-Q101-001	Human body model (contact mode)	$C = 100 \text{ pF}, R = 1.5 \text{ k}\Omega$		НЗВ	> 8 kV				
AEC-Q101-005	Charged device model (CDM)	V = 500 V	V	C3	> 1 kV				
IEC 61000-4-2 (2)	Contact mode	C = 150 pF, R = 330 Ω	V <sub>C</sub>	4	> 8 kV				
	Air-discharge mode (1)	C = 150 pF, R = 330 $\Omega$		4	> 15 kV				

### Notes

 $^{(1)}$  Immunity to IEC 61000-4-2 air discharge mode has a typical performance > 30 kV

(2) System ESD standard

THERMAL CHARACTERISTICS (T <sub>A</sub> = 25 °C unless otherwise noted)							
PARAMETER SYMBOL TYP. MAX. UNIT							
Thermal resistance per diode	R <sub>0JA</sub> (1)(2)	80	=	°C/W			
Thermal resistance per diode	R <sub>0JM</sub> (3)	3.0	4.0	C/VV			

#### Notes

- $^{(1)}$  The heat generated must be less than thermal conductivity from junction-to-ambient:  $dP_D/dT_J < 1/R_{\theta JA}$
- (2) Thermal resistance junction-to-ambient to follow JEDEC® 51-2A, device mounted on FR4 PCB, 2 oz., standard footprint
- (3) Thermal resistance junction-to-mount to follow JEDEC® 51-14 transient dual interface test method (TDIM)

ORDERING INFORMATION (Example)						
PREFERRED P/N UNIT WEIGHT (g) PREFERRED PACKAGE CODE BASE QUANTITY DELIVERY MODE						
R3T2FPHM3/I (1)	0.10	I	6000	13" diameter plastic tape and reel		

#### Note

(1) AEC-Q101 qualified



## **RATINGS AND CHARACTERISTICS CURVES FOR RECTIFIERS** (T<sub>A</sub> = 25 °C unless otherwise noted)

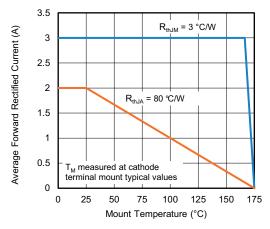


Fig. 1 - Maximum Forward Derating Curve

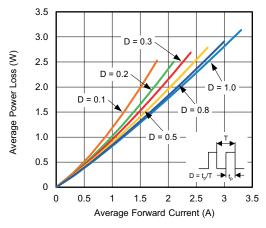


Fig. 2 - Forward Power Loss Characteristics

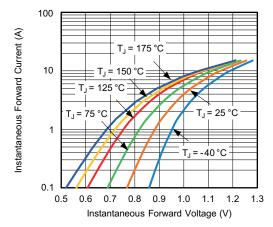


Fig. 3 - Typical Instantaneous Forward Characteristics

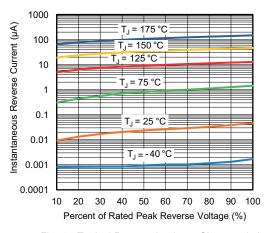


Fig. 4 - Typical Reverse Leakage Characteristics

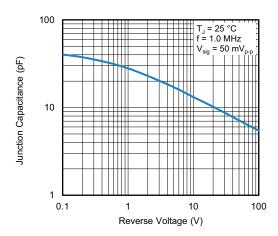


Fig. 5 - Typical Junction Capacitance

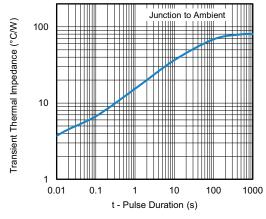


Fig. 6 - Typical Transient Thermal Impedance



## **RATINGS AND CHARACTERISTICS CURVES FOR TVS** (T<sub>A</sub> = 25 °C unless otherwise noted)

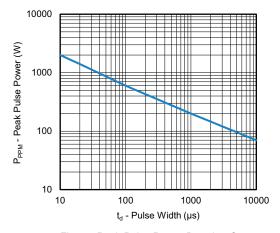


Fig. 7 - Peak Pulse Power Derating Curve

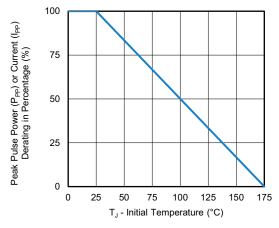


Fig. 8 - Pulse Power or Current vs. Initial Junction Temperature

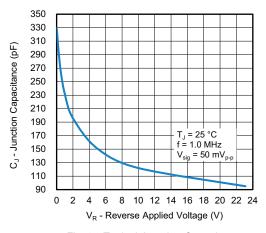


Fig. 9 - Typical Junction Capacitance

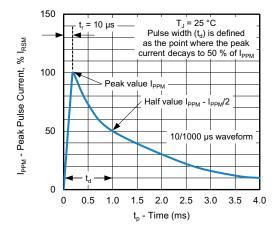
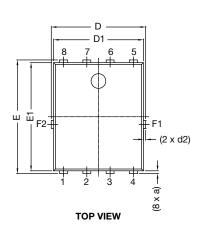


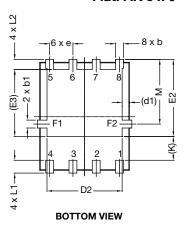
Fig. 10 - Pulse Waveform

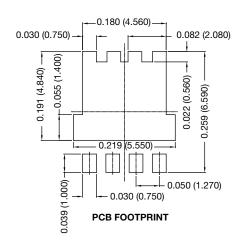


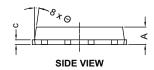
## **PACKAGE OUTLINE DIMENSIONS** in inches (millimeters)

### FlatPAK 5 x 6









DIM		INCHES			MILLIMETERS			
DIM.	MIN.	NOM.	MAX.	MIN.	NOM.	MAX.		
Α	0.035	0.039	0.043	0.89	0.99	1.09		
(a)	-	0.006	=	-	0.15	-		
b	0.013	0.017	0.020	0.32	0.43	0.52		
b1	0.013	0.017	0.020	0.32	0.43	0.52		
С	0.008	-	0.014	0.20	-	0.35		
D	0.197	0.203	0.209	5.00	5.15	5.30		
D1	0.189	0.193	0.197	4.80	4.90	5.00		
D2	0.154	0.161	0.169	3.90	4.10	4.30		
(d1)	-	0.016	=	-	0.40	-		
(d2)	=	0.005	=	-	0.125	-		
Е	0.238	0.244	0.250	6.05	6.20	6.35		
E1	0.228	0.232	0.236	5.80	5.90	6.00		
E2	0.157	0.165	0.173	4.00	4.20	4.40		
(E3)	-	0.144	-	-	3.65	-		
е		0.050 BSC			1.27 BSC			
(K)	0.039	-	=	1.00	-	-		
L1	0.019	-	0.043	0.48	-	1.10		
L2	0.012	-	0.031	0.30	-	0.80		
М	0.128	0.138	0.148	3.25	3.50	3.75		
Θ	0°	-	10°	0°	-	10°		

#### Notes

- Dimensioning and tolerancing per ASME Y14.5-2009
- Dimensions D1 and E1 do not include mold flash or gate burrs
- Dimension (XX) means reference only



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